



Docket No.: 60188-808

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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| In re Application of | : | Customer Number: 20277 |
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| Masanori SHIRAHAMA, et al. | : | Confirmation Number: |
| | : | |
| Serial No.: 10/802,860 | : | Group Art Unit: 2818 |
| | : | |
| Filed: March 18, 2004 | : | Examiner: |
| | : | |
| For: SEMICONDUCTOR STORAGE CIRCUIT AND LAYOUT METHOD FOR THE SAME | | |

CORRECTED INFORMATION DISCLOSURE STATEMENT

Mail Stop Information Disclosure
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

In accordance with the provisions of 37 C.F.R. 1.56, 1.97 and 1.98, the attention of the Patent and Trademark Office is hereby directed to the documents listed on the attached form PTO-1449. It is respectfully requested that the documents be expressly considered during the prosecution of this application, and that the documents be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

This Information Disclosure Statement is being filed within three months of the U.S. filing date OR before the mailing date of a first Office Action on the merits. No certification or fee is required.

The relevance of Japanese Laid-Open Patent Publication No. 2002-25251 is discussed in the present specification.

Serial No.: 10/802,860

Please note that US 2003/0072201 has been corrected on the attached PTO-1449 Form and a copy attached.

Please charge any shortage in fees due in connection with the filing of this paper, including extension of time fees, to Deposit Account 500417 and please credit any excess fees to such deposit account.

Respectfully submitted,

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Date: July 12, 2004

CORRECTED

SHEET 1 OF 1

| INFORMATION DISCLOSURE CITATION IN AN APPLICATION (PTO-1449) | | | | ATTY. DOCKET NO. 60188-808 | | SERIAL NO. 10/802,860 | |
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| | | | | APPLICANT Masanori SHIRAHAMA, et al. | | | |
| | | | | FILING DATE March 18, 2004 | | GROUP 2818 | |
| U.S. PATENT DOCUMENTS | | | | | | | |
| EXAMINER'S INITIALS | CITE NO. | Document Number Number-Kind Code ² (if known) | Publication Date MM-DD-YYYY | Name of Patentee or Applicant of Cited Document | Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear | | |
| | | US | US 2003/0072201 A1 | 04/17/2003 | Inaba | | |
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| FOREIGN PATENT DOCUMENTS | | | | | | | |
| EXAMINER'S INITIALS | CITE NO. | Foreign Patent Document Country Codes-Number 4-Kind Codes (if known) | Publication Date MM-DD-YYYY | Name of Patentee or Applicant of Cited Document | Pages, Columns, Lines Where Relevant Figures Appear | Translation | |
| | | | | | | Yes | No |
| | | JP 2002-25251 A (w/ English Abstract) | 01/25/2002 | MITSUBISHI ELECTRIC CORP. & MITSUBISHI ELECTRIC ENGINEERING CO., LTD. | | | |
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| OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.) | | | | | | | |
| EXAMINER'S INITIALS | CITE NO. | Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published. | | | | | |
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| EXAMINER | | | | DATE CONSIDERED | | | |

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.
 1 Applicant's unique citation designation number (optional). 2 Applicant is to place a check mark here if English language Translation is attached.